

The Translation of Logic Languages

26 The Language Proll

Here, we just consider the core language **Proll** (“Prolog-light” :-). In particular, we omit:

- arithmetic;
- the cut operator;
- self-modification of programs through **assert** and **retract**.

Example:

$\text{bigger}(X, Y) \leftarrow X = \textit{elephant}, Y = \textit{horse}$
 $\text{bigger}(X, Y) \leftarrow X = \textit{horse}, Y = \textit{donkey}$
 $\text{bigger}(X, Y) \leftarrow X = \textit{donkey}, Y = \textit{dog}$
 $\text{bigger}(X, Y) \leftarrow X = \textit{donkey}, Y = \textit{monkey}$
 $\text{is_bigger}(X, Y) \leftarrow \text{bigger}(X, Y)$
 $\text{is_bigger}(X, Y) \leftarrow \text{bigger}(X, Z), \text{is_bigger}(Z, Y)$
? $\text{is_bigger}(\textit{elephant}, \textit{dog})$

A More Realistic Example:

$\text{app}(X, Y, Z) \leftarrow X = [], Y = Z$

$\text{app}(X, Y, Z) \leftarrow X = [H|X'], Z = [H|Z'], \text{app}(X', Y, Z')$

? $\text{app}(X, [Y, c], [a, b, Z])$

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Remark:

$[]$ \equiv the atom **empty list**

$[H|Z]$ \equiv **binary** constructor application

$[a, b, Z]$ \equiv shortcut for: $[a|[b|[Z|[]]]]$

A program p is constructed as follows:

$$\begin{aligned}t & ::= a \mid X \mid _ \mid f(t_1, \dots, t_n) \\g & ::= p(t_1, \dots, t_k) \mid X = t \\c & ::= p(X_1, \dots, X_k) \leftarrow g_1, \dots, g_r \\p & ::= c_1 \dots c_m ? g\end{aligned}$$

- A **term** t either is an atom, a variable, an anonymous variable or a constructor application.
- A **goal** g either is a literal, i.e., a predicate call, or a unification.
- A **clause** c consists of a **head** $p(X_1, \dots, X_k)$ with predicate name and list of formal parameters together with a **body**, i.e., a sequence of goals.
- A **program** consists of a sequence of clauses together with a single goal as **query**.

Procedural View of Proll programs:

goal	==	procedure call
predicate	==	procedure
body	==	definition
term	==	value
unification	==	basic computation step
binding of variables	==	side effect

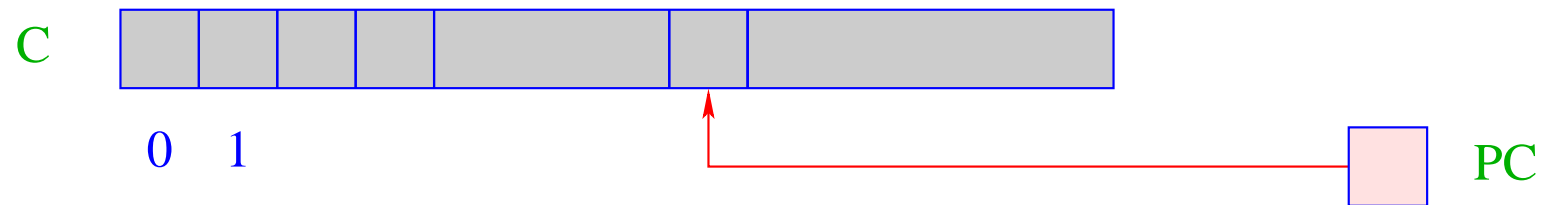
Note: Predicate calls ...

- ... do not have a return value.
- ... affect the caller through side effects only :-)
- ... may fail. Then the next definition is tried :-))

⇒ backtracking

27 Architecture of the WiM:

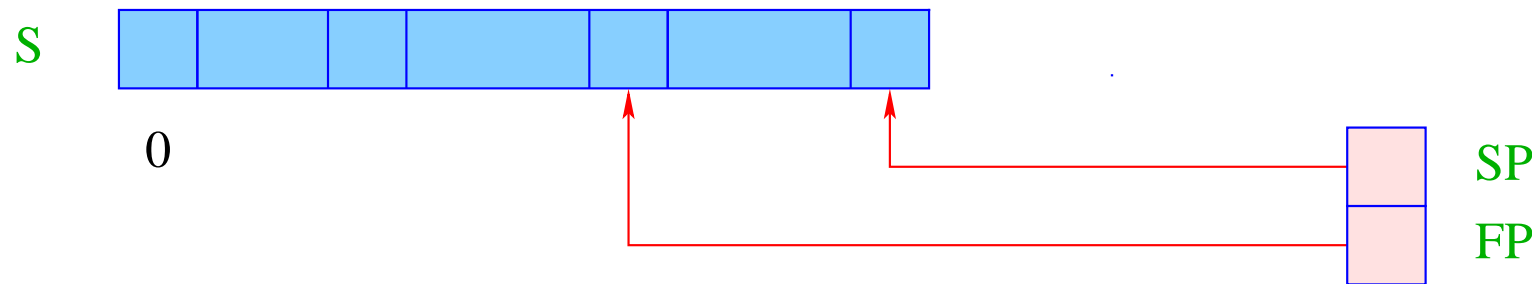
The Code Store:



C = Code store – contains WiM program;
every cell contains one instruction;

PC = Program Counter – points to the next instruction to executed;

The Runtime Stack:



S = Runtime **S**tack – every cell may contain a value or an address;

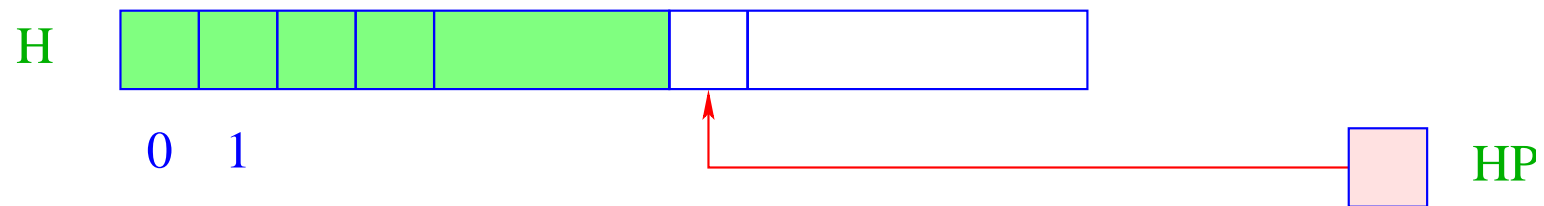
SP = **S**tack **P**ointer – points to the topmost occupied cell;

FP = **F**rame **P**ointer – points to the current stack frame.

Frames are created for predicate calls,

contain cells for each variable of the current clause

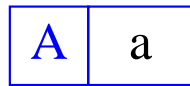
The Heap:



H = Heap for dynamicly constructed terms;

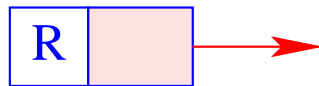
HP = Heap-Pointer – points to the first free cell;

- The heap is maintained like a **stack** as well :-)
- A **new**-instruction allocates an object in **H**.
- Objects are **tagged** with their types (as in the **MaMa**) ...



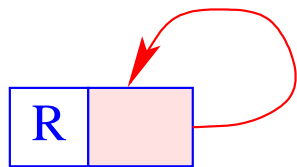
atom

1 cell



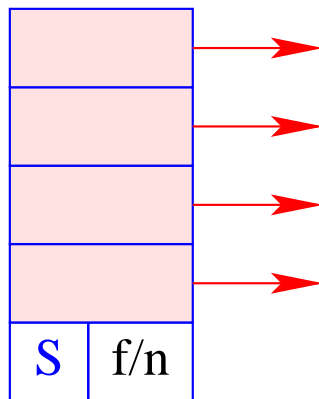
variable

1 cell



unbound variable

1 cell



structure

(n+1) cells

28 Construction of Terms in the Heap

Parameter terms of goals (calls) are constructed in the heap before passing.

Assume that the **address environment** ρ returns, for each clause variable X its address (relative to **FP**) on the stack. Then $\text{code}_A t \rho$ should ...

- construct (a presentation of) t in the heap; and
- return a reference to it on top of the stack.

Idea:

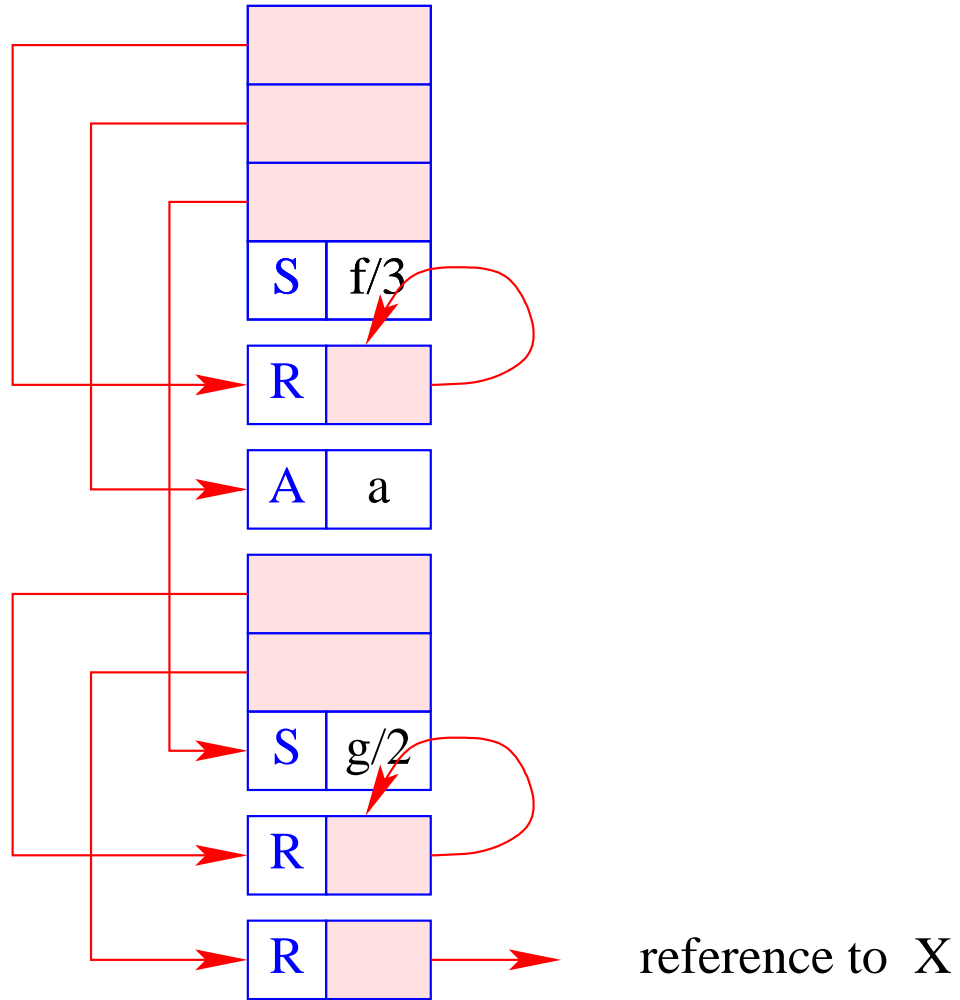
- Construct the tree during a **post-order** traversal of t
- with one instruction for each new node!

Example: $t \equiv f(g(X, Y), a, Z)$.

Assume that X is **initialized**, i.e., $S[\text{FP} + \rho X]$ contains already a reference, Y and Z are not yet initialized.

Representing

$$t \equiv f(g(X, Y), a, Z) :$$



For a distinction, we mark occurrences of already initialized variables through **over-lining** (e.g. \bar{X}).

Note: Arguments are always initialized!

Then we define:

$\text{code}_A a \rho$	$=$	$\text{putatom } a$	$\text{code}_A f(t_1, \dots, t_n) \rho$	$=$	$\text{code}_A t_1 \rho$
$\text{code}_A X \rho$	$=$	$\text{putvar } (\rho X)$			\dots
$\text{code}_A \bar{X} \rho$	$=$	$\text{putref } (\rho X)$			$\text{code}_A t_n \rho$
$\text{code}_A _ \rho$	$=$	putanon			$\text{putstruct } f/n$

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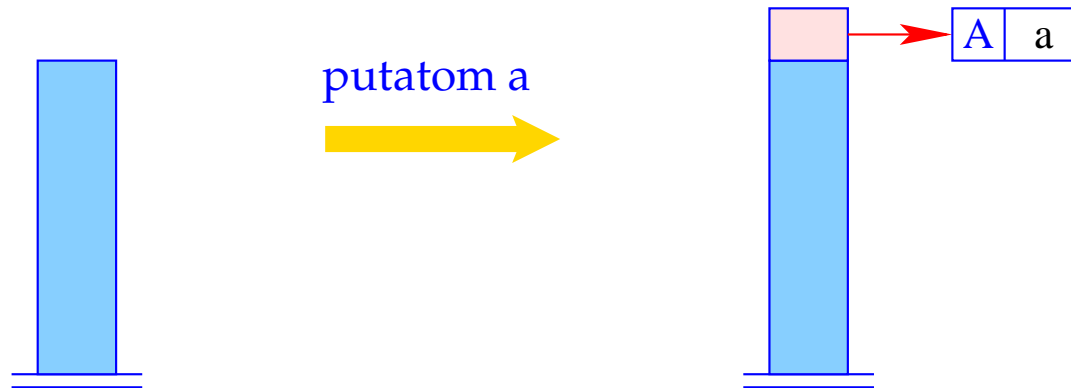
Then we define:

$$\begin{array}{ll} \text{code}_A a \rho & = \text{putatom } a & \text{code}_A f(t_1, \dots, t_n) \rho & = \text{code}_A t_1 \rho \\ \text{code}_A X \rho & = \text{putvar } (\rho X) & & \dots \\ \text{code}_A \bar{X} \rho & = \text{putref } (\rho X) & & \text{code}_A t_n \rho \\ \text{code}_A _ \rho & = \text{putanon} & & \text{putstruct } f/n \end{array}$$

For $f(g(\bar{X}, Y), a, Z)$ and $\rho = \{X \mapsto 1, Y \mapsto 2, Z \mapsto 3\}$ this results in the sequence:

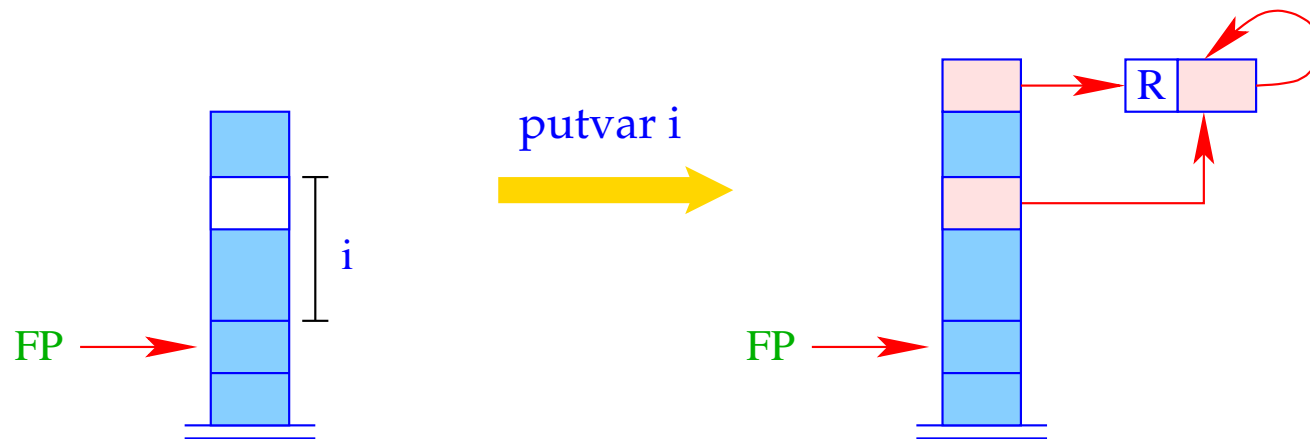
putref 1	putatom a
putvar 2	putvar 3
putstruct g/2	putstruct f/3

The instruction `putatom a` constructs an atom in the heap:



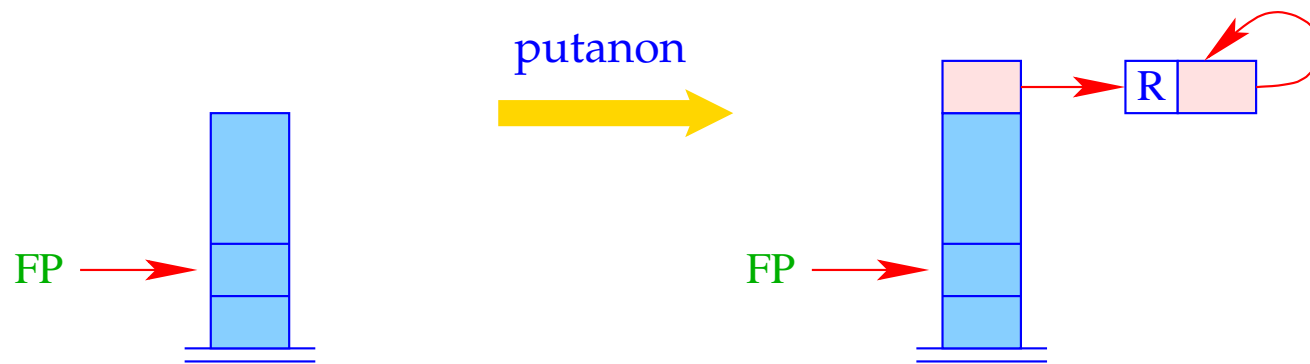
$SP++; S[SP] = \text{new } (A,a);$

The instruction `putvar i` introduces a new unbound variable and additionally initializes the corresponding cell in the stack frame:



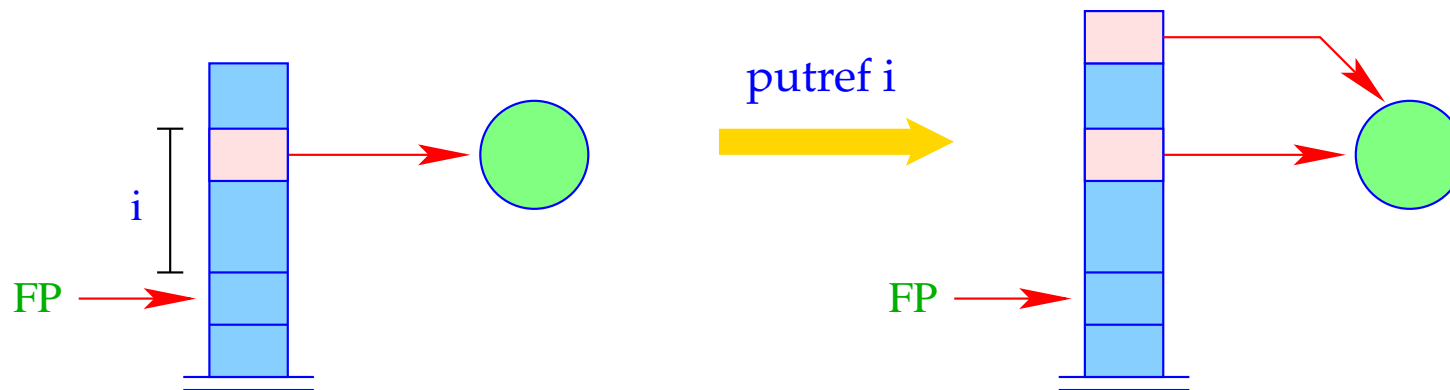
```
SP = SP + 1;  
S[SP] = new (R, HP);  
S[FP + i] = S[SP];
```

The instruction `putanon` introduces a new unbound variable but does not store a reference to it in the stack frame:



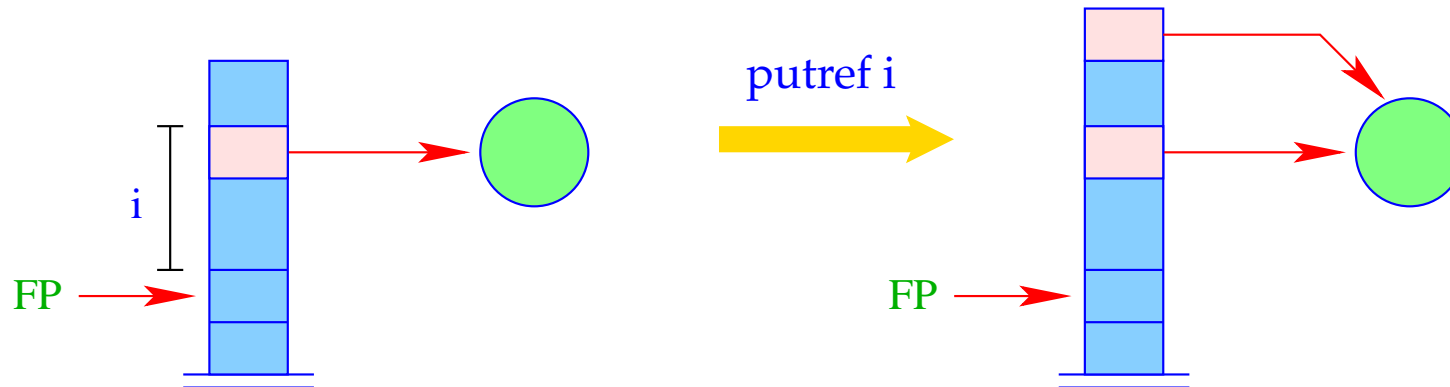
```
SP = SP + 1;  
S[SP] = new (R, HP);
```

The instruction `putref i` pushes the value of the variable onto the stack:



```
SP = SP + 1;  
S[SP] = deref S[FP + i];
```

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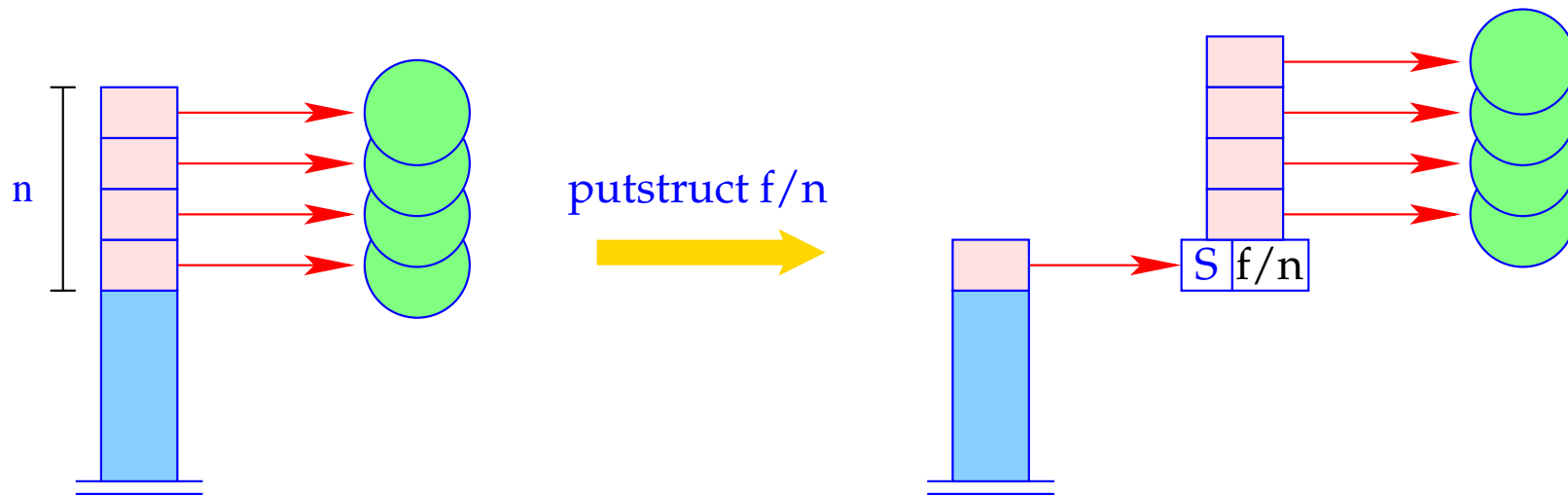


```
SP = SP + 1;  
S[SP] = deref S[FP + i];
```

The auxiliary function `deref` contracts **chains** of references:

```
ref deref (ref v) {  
    if (H[v]==(R,w) && v!=w) return deref (w);  
    else return v;  
}
```

The instruction `putstruct i` builds a constructor application in the heap:



```

v = new (S, f, n);
SP = SP - n + 1;
for (i=1; i<=n; i++)
    H[v + i] = S[SP + i - 1];
S[SP] = v;

```

Remarks:

- The instruction `putref i` does not just push the reference from $S[\text{FP} + i]$ onto the stack, but also dereferences it as much as possible
 \implies maximal contraction of reference chains.
- In constructed terms, references always point to **smaller** heap addresses.
Also otherwise, this will be often the case. Sadly enough, it cannot be **guaranteed** in general :-)

29 The Translation of Literals (Goals)

Idea:

- Literals are treated as **procedure calls**.
- We first allocate a stack frame.
- Then we construct the actual parameters (in the heap)
- ... and store references to these into the stack frame.
- Finally, we jump to the code for the procedure/predicate.

```
codeG p(t1, ..., tk) ρ =   mark B           // allocates the stack frame
                               codeA t1 ρ
                               ...
                               codeA tk ρ
                               call p/k         // calls the procedure p/k
B : ...
```



```

codeG p(t1, ..., tk) ρ =   mark B           // allocates the stack frame
                               codeA t1 ρ
                               ...
                               codeA tk ρ
                               call p/k         // calls the procedure p/k
                               B : ...

```

Example: $p(a, X, g(\bar{X}, Y))$ with $\rho = \{X \mapsto 1, Y \mapsto 2\}$

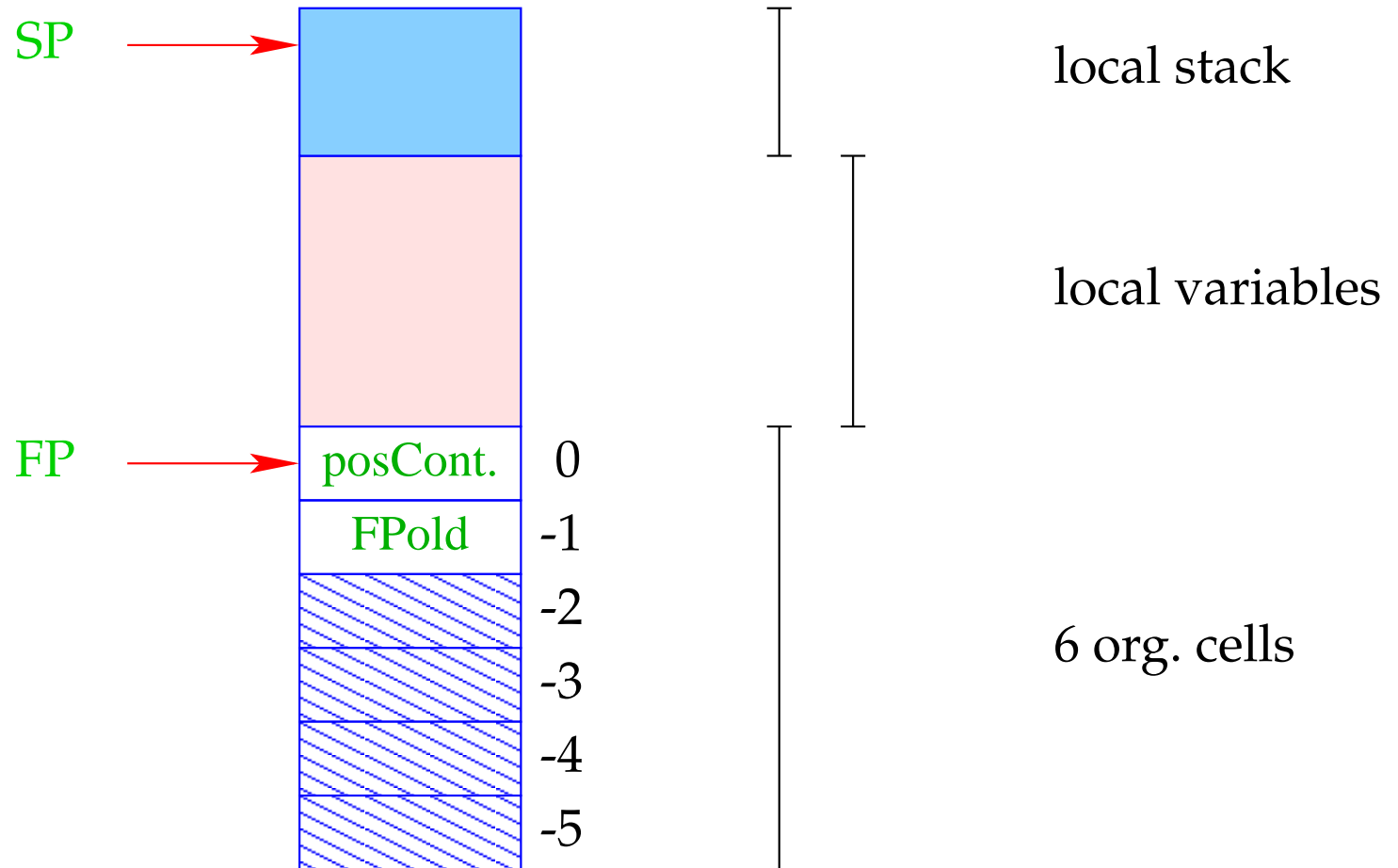
We obtain:

```

mark B           putref 1           call p/3
putatom a       putvar 2           B: ...
putvar 1        putstruct g/2

```

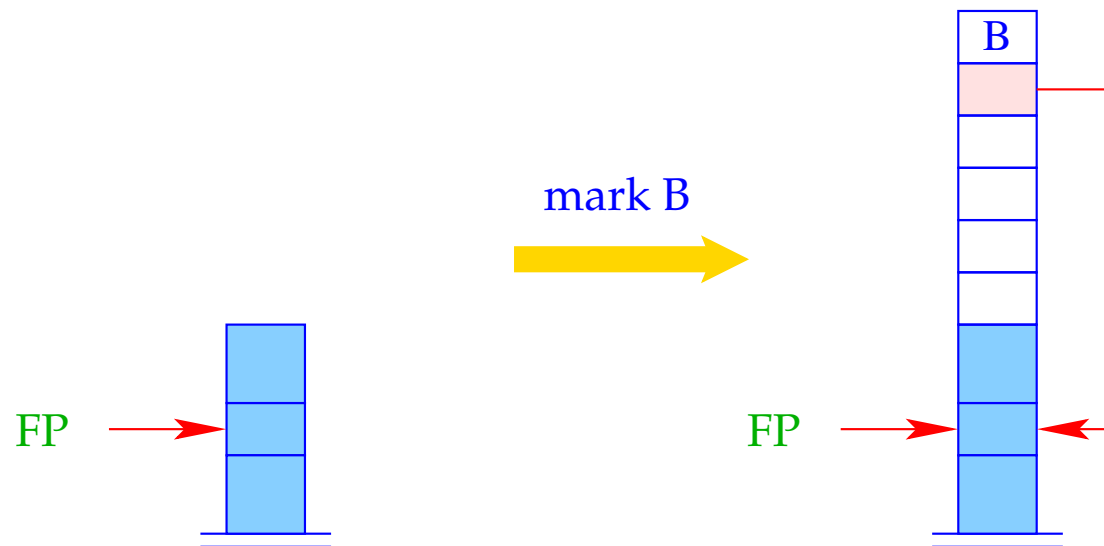
Stack Frame of the WiM:



Remarks:

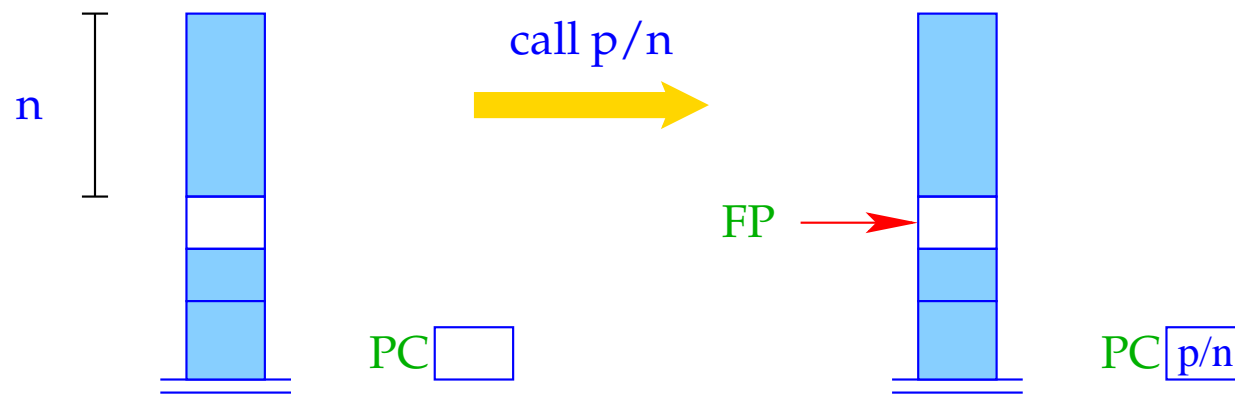
- The **positive** continuation address records where to continue after successful treatment of the goal.
- Additional organizational cells are needed for the implementation of **backtracking**
 \implies will be discussed at the translation of predicates.

The instruction `mark B` allocates a new stack frame:



```
SP = SP + 6;  
S[SP] = B; S[SP-1] = FP;
```

The instruction `call p/n` calls the n -ary predicate p :



$$\begin{aligned} \text{FP} &= \text{SP} - n; \\ \text{PC} &= p/n; \end{aligned}$$

30 Unification

Convention:

- By \tilde{X} , we denote an occurrence of X ;
it will be translated differently depending on whether the variable is initialized or not.
- We introduce the macro `put \tilde{X} ρ` :

`put X ρ` = `putvar (ρ X)`

`put $_$ ρ` = `putanon`

`put \bar{X} ρ` = `putref (ρ X)`

Let us translate the unification $\tilde{X} = t$.

Idea 1:

- Push a reference to (the binding of) X onto the stack;
- Construct the term t in the heap;
- Invent a new instruction implementing the unification :-)

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- Construct the term t in the heap;
- Invent a new instruction implementing the unification :-)

$$\text{code}_G (\tilde{X} = t) \rho = \begin{array}{l} \text{put } \tilde{X} \rho \\ \text{code}_A t \rho \\ \text{unify} \end{array}$$

Example:

Consider the equation:

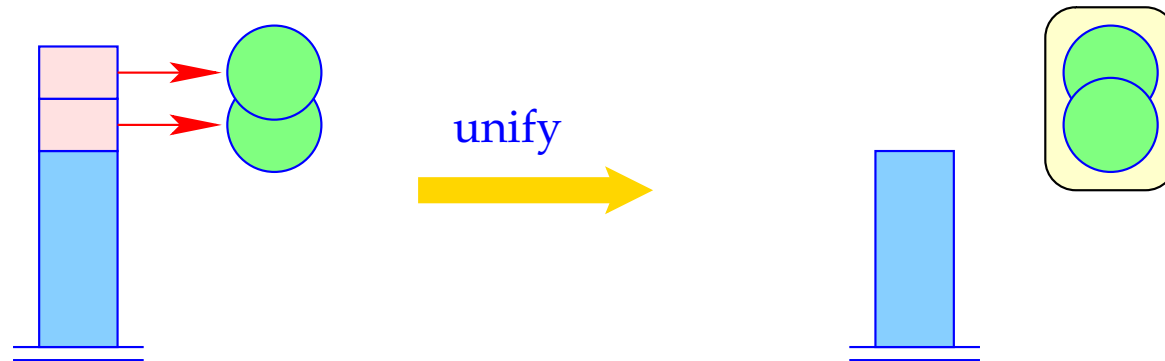
$$\bar{U} = f(g(\bar{X}, Y), a, Z)$$

Then we obtain for an address environment

$$\rho = \{X \mapsto 1, Y \mapsto 2, Z \mapsto 3, U \mapsto 4\}$$

putref 4	putref 1	putatom a	unify
	putvar 2	putvar 3	
	putstruct g/2	putstruct f/3	

The instruction `unify` calls the `run-time` function `unify()` for the topmost two references:



```
unify (S[SP-1], S[SP]);  
SP = SP-2;
```

The Function `unify()`

- ... takes two heap addresses.
For each call, we guarantee that these are **maximally de-referenced**.
- ... checks whether the two addresses are already **identical**.
If so, does nothing **:-)**
- ... binds **younger variables** (larger addresses) to **older variables** (smaller addresses);
- ... when binding a variable to a term, checks whether the variable occurs inside the term \implies **occur-check**;
- ... **records** newly created bindings;
- ... may **fail**. Then **backtracking** is initiated.

```

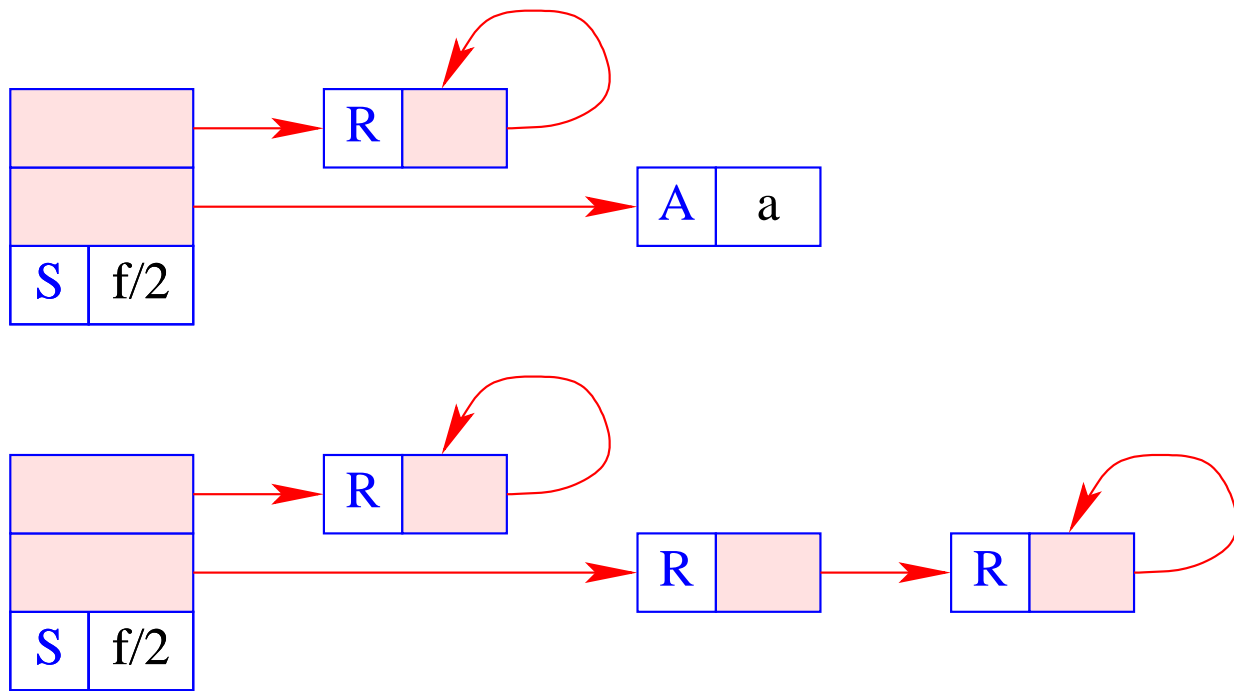
bool unify (ref u, ref v) {
    if (u == v) return true;
    if (H[u] == (R,_)) {
        if (H[v] == (R,_)) {
            if (u>v) {
                H[u] = (R,v); trail (u); return true;
            } else {
                H[v] = (R,u); trail (v); return true;
            }
        } elseif (check (u,v)) {
            H[u] = (R,v); trail (u); return true;
        } else {
            backtrack(); return false;
        }
    }
}
...

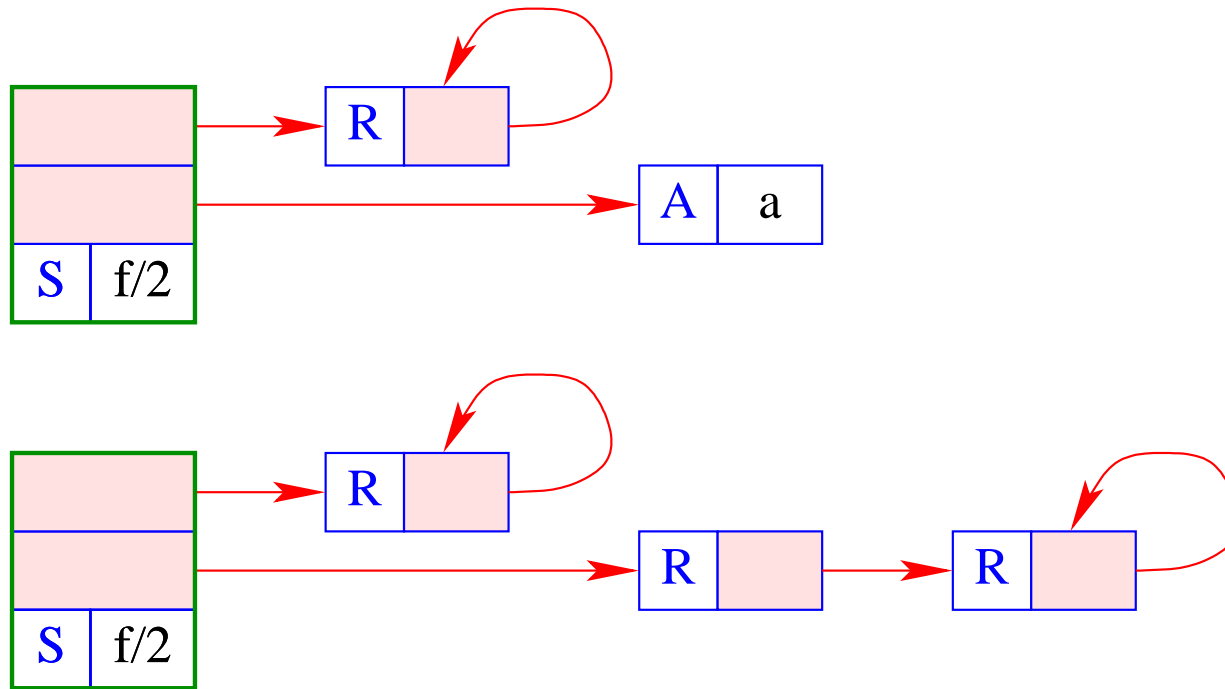
```

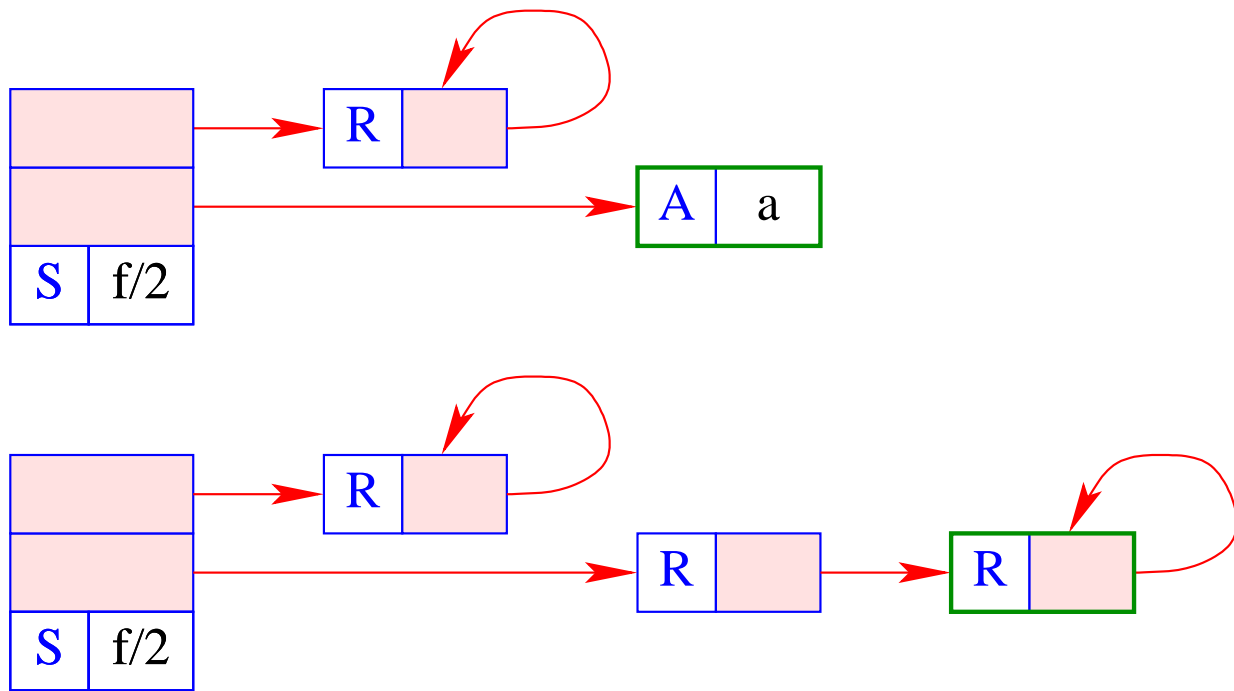
```

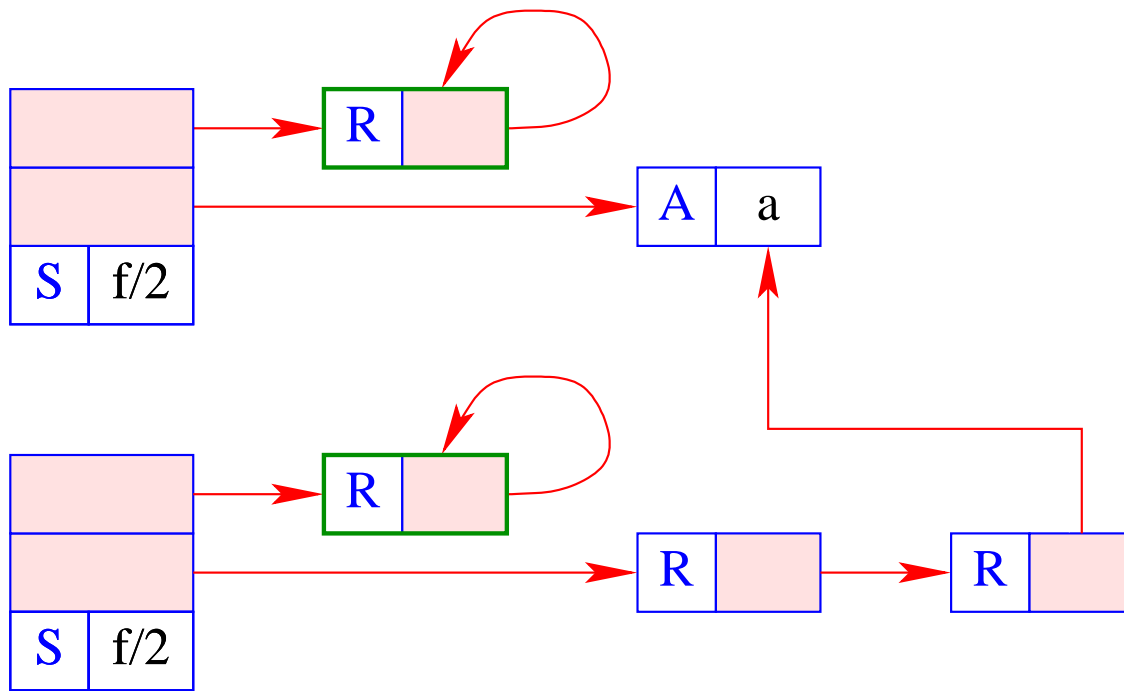
...
if ((H[v] == (R,_)) {
    if (check (v,u)) {
        H[v] = (R,u); trail (v); return true;
    } else {
        backtrack(); return false;
    }
}
if (H[u]==(A,a) && H[v]==(A,a))
    return true;
if (H[u]==(S, f/n) && H[v]==(S, f/n)) {
    for (int i=1; i<=n; i++)
        if(!unify (deref (H[u+i]), deref (H[v+i])) return false;
    return true;
}
backtrack(); return false;
}

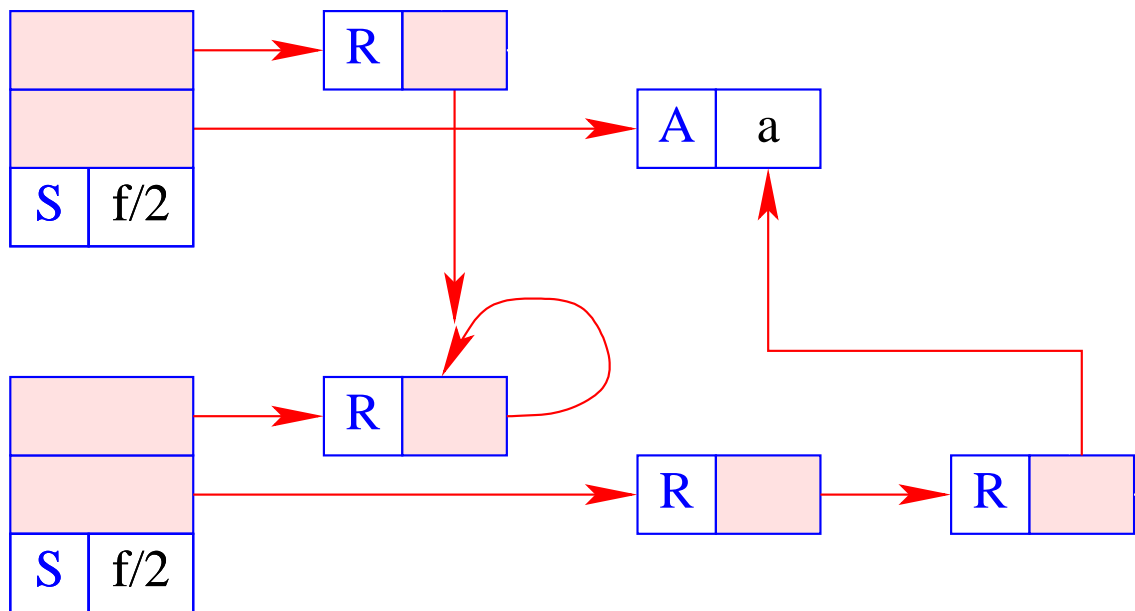
```











- The run-time function `trail()` **records** the a potential new binding.
- The run-time function `backtrack()` initiates **backtracking**.
- The auxiliary function `check()` performs the **occur-check**: it tests whether a variable (the first argument) **occurs inside** a term (the second argument).
- Often, this check is skipped, i.e.,

```
bool check (ref u, ref v) { return true; }
```

Otherwise, we could implement the run-time function `check()` as follows:

```
bool check (ref u, ref v) {
    if (u == v) return false;
    if (H[v] == (S, f/n)) {
        for (int i=1; i<=n; i++)
            if (!check(u, deref (H[v+i])))
                return false;
    }
    return true;
}
```