

Discussion:

- The translation of an equation $\tilde{X} = t$ is very simple :-)
- Often the constructed cells immediately become garbage :-)

Idea 2:

- Push a reference to the run-time binding of the left-hand side onto the stack.
- Avoid to construct sub-terms of t whenever possible !
- Translate each node of t into an instruction which performs the unification with this node !!

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- Often the constructed cells immediately become **garbage** :-)

Idea 2:

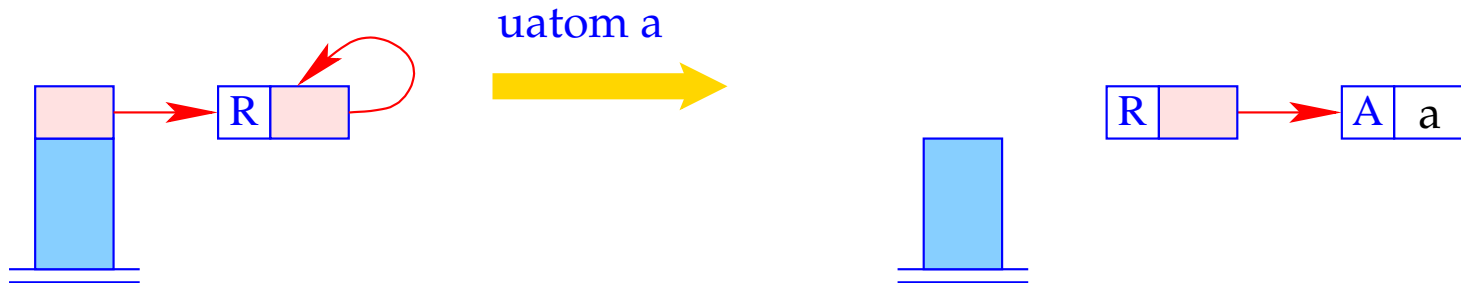
- Push a reference to the run-time binding of the left-hand side onto the stack.
- Avoid to construct sub-terms of t whenever possible !
- Translate each node of t into an instruction which performs the unification with this node !!

$$\text{code}_G (\tilde{X} = t) \rho = \text{put } \tilde{X} \rho \\ \text{code}_U t \rho$$

Let us first consider the unification code for atoms and variables only:

```
codeU a ρ = uatom a  
codeU X ρ = uvar (ρ X)  
codeU _ ρ = pop  
codeU  $\bar{X}$  ρ = uref (ρ X)  
... // to be continued :-)
```

The instruction `uatom a` implements the unification with the atom `a`:



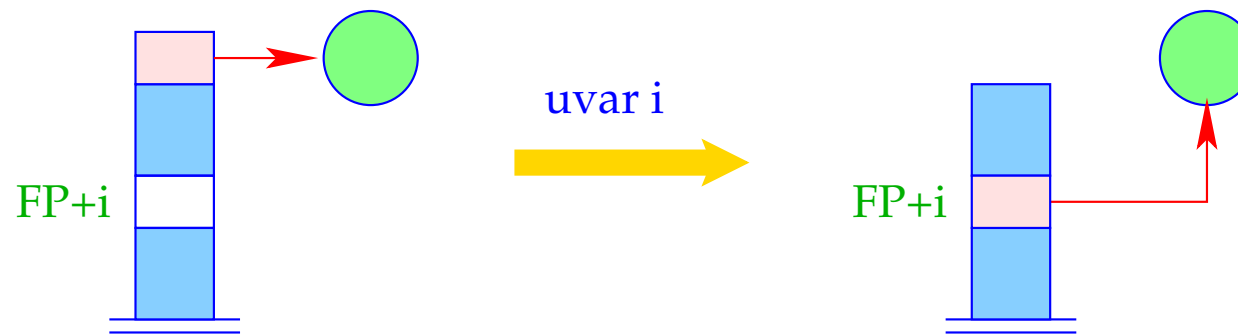
```

v = S[SP]; SP--;
switch (H[v]) {
case (A, a):    break;
case (R, _):   H[v] = (R, new (A, a));
               trail (v); break;
default:      backtrack();
}

```

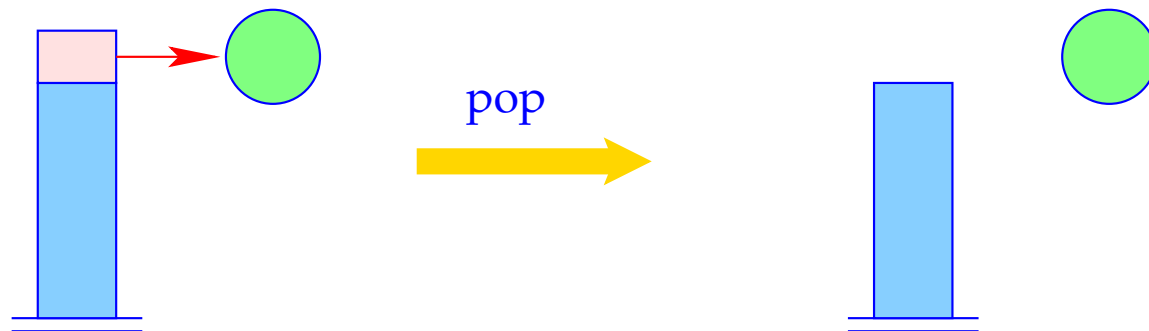
- The run-time function `trail()` records the a potential new binding.
- The run-time function `backtrack()` initiates backtracking.

The instruction `uvar i` implements the unification with an un-initialized variable:



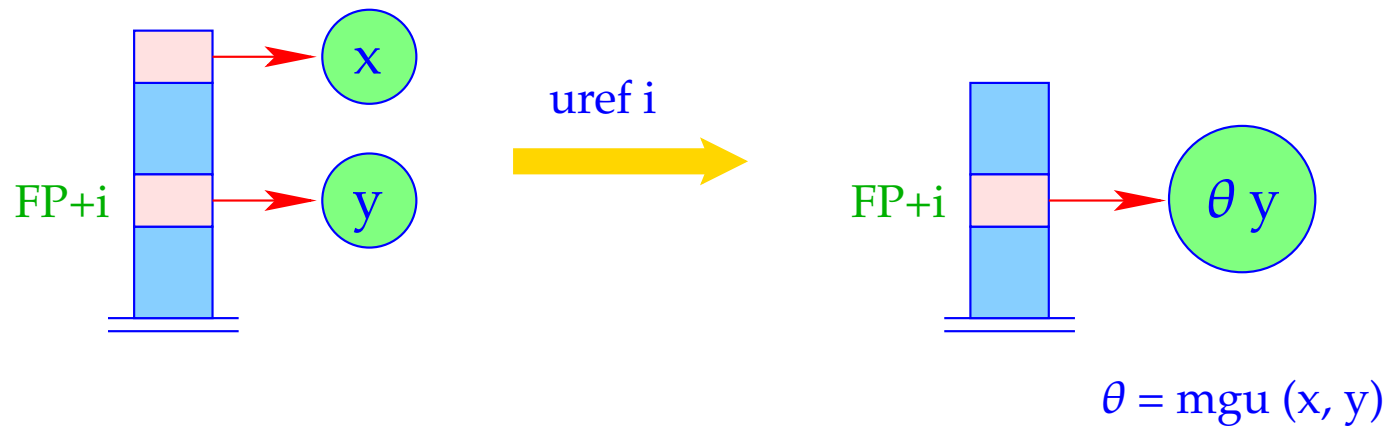
$$S[FP+i] = S[SP]; SP--;$$

The instruction `pop` implements the unification with an anonymous variable. It always succeeds :-)



`SP--;`

The instruction `uref i` implements the unification with an initialized variable:



```
unify (S[SP], deref (S[FP+i]));  
SP--;
```

It is only here that the run-time function `unify()` is called :-)

- The unification code performs a **pre-order** traversal over t .
- In case, execution hits at an unbound variable, we **switch** from checking to building :-)

```

codeU f(t1, ..., tn) ρ =   ustruct f/n A           // test
                             son 1
                             codeU t1 ρ
                             ...
                             son n
                             codeU tn ρ
                             up B
A : check ivars(f(t1, ..., tn)) ρ   // occur-check
    codeA f(t1, ..., tn) ρ       // building !!
    bind                               // creation of bindings
B : ...

```


The Building Block:

Before constructing the new (sub-) term t' for the binding, we must exclude that it contains the variable X' on top of the stack !!!

This is the case iff **the binding** of no variable inside t' contains (a reference to) X' .

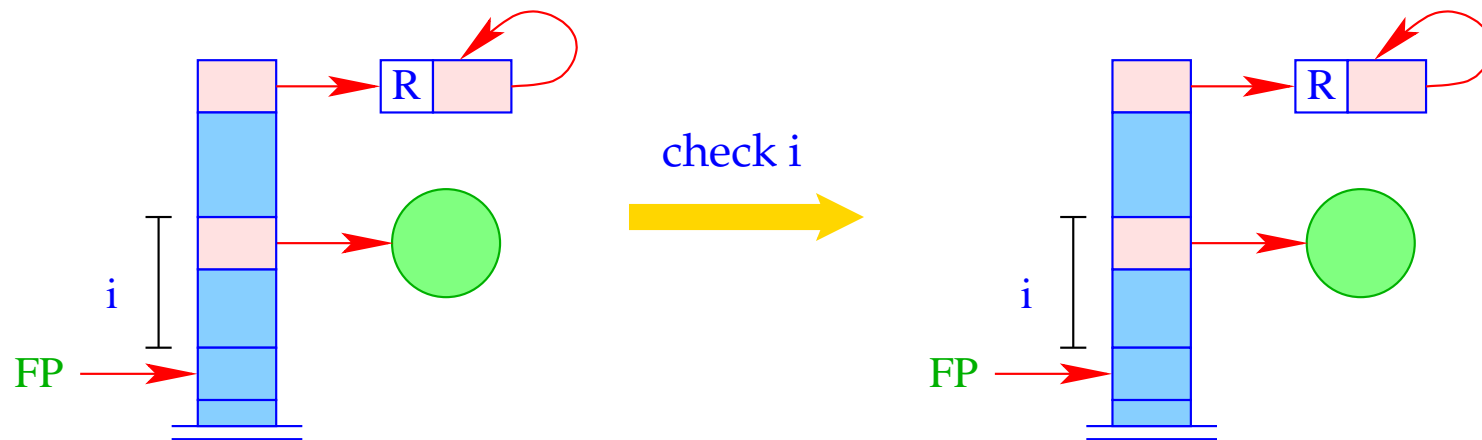
\implies $ivars(t')$ returns the set of **already initialized** variables of t .

\implies The macro **check** $\{Y_1, \dots, Y_d\} \rho$ generates the necessary tests on the variables Y_1, \dots, Y_d :

$$\begin{aligned} \text{check } \{Y_1, \dots, Y_d\} \rho &= \text{check } (\rho Y_1) \\ &\quad \text{check } (\rho Y_2) \\ &\quad \dots \\ &\quad \text{check } (\rho Y_d) \end{aligned}$$

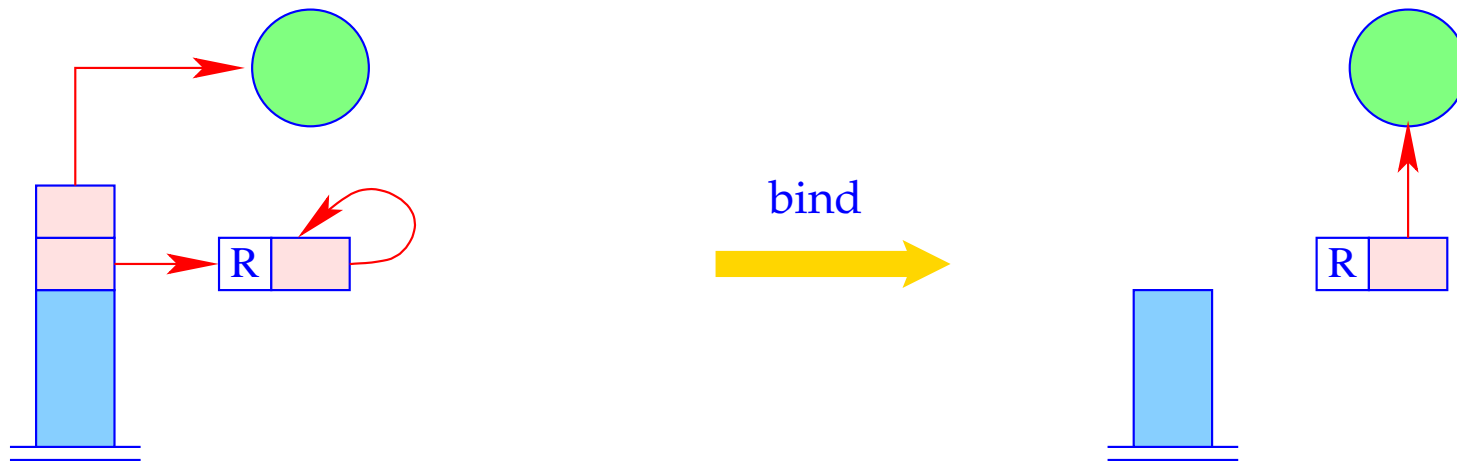
The instruction `check i` checks whether the (unbound) variable on top of the stack occurs inside the term bound to variable `i`.

If so, unification fails and **backtracking** is caused:



```
if (!check (S[SP], deref S[FP+i]))  
    backtrack();
```

The instruction `bind` terminates the building block. It binds the (unbound) variable to the constructed term:



```
H[S[SP-1]] = (R, S[SP]);  
trail (S[SP-1]);  
SP = SP - 2;
```

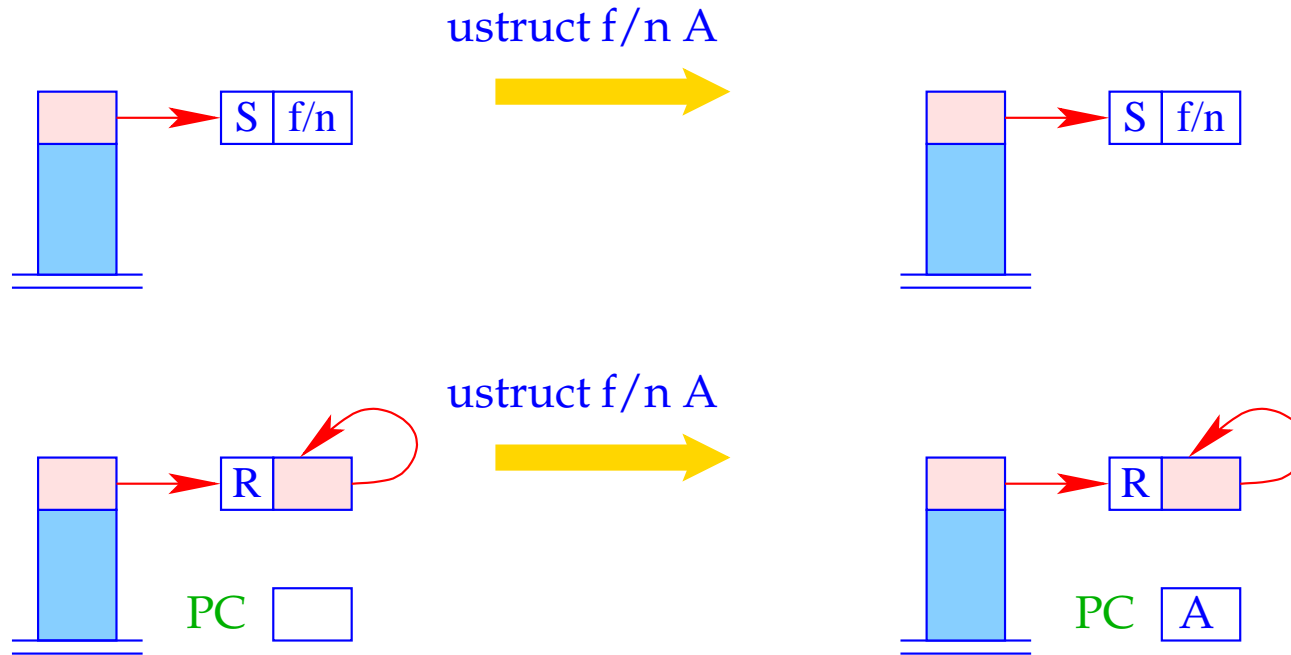
The Pre-Order Traversal:

- First, we **test** whether the topmost reference is an unbound variable. If so, we jump to the building block.
- Then we compare the root node with the constructor **f/n**.
- Then we **recursively descend** to the children.
- Then we **pop** the stack and proceed behind the unification code:

Once again the unification code for constructed terms:

```
codeU f(t1, ..., tn) ρ =    ustruct f/n A                // test
                                son 1                // recursive descent
                                codeU t1 ρ
                                ...
                                son n                // recursive descent
                                codeU tn ρ
                                up B                  // ascent to father
A : check ivars(f(t1, ..., tn)) ρ
    codeA f(t1, ..., tn) ρ
    bind
B : ...
```

The instruction `ustruct i` implements the test of the root node of a structure:



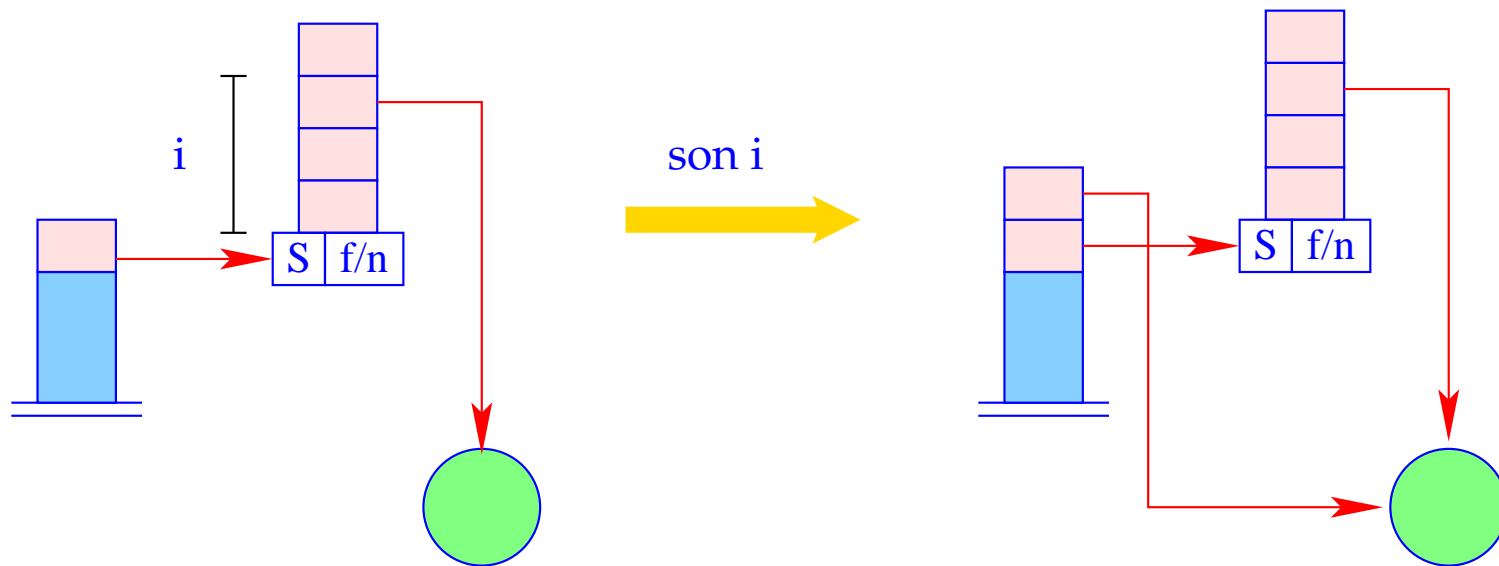
```

switch (H[S[SP]]) {
  case (S, f/n):  break;
  case (R, _):   PC = A; break;
  default:       backtrack();
}

```

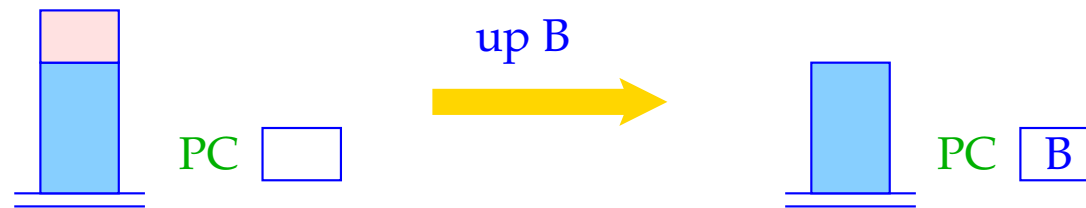
... the argument reference is **not yet** popped :-)

The instruction `son i` pushes the (reference to the) i -th sub-term from the structure pointed at from the topmost reference:



$S[SP+1] = \text{deref}(H[S[SP]+i]); SP++;$

It is the instruction `up B` which finally pops the reference to the structure:



`SP--; PC = B;`

The continuation address `B` is the next address after the `build`-section.

Example:

For our example term $f(g(\bar{X}, Y), a, Z)$ and
 $\rho = \{X \mapsto 1, Y \mapsto 2, Z \mapsto 3\}$ we obtain:

ustruct f/3 A_1	up B_2	B_2 :	son 2	putvar 2	
son 1			uatom a	putstruct g/2	
ustruct g/2 A_2	A_2 :	check 1	son 3	putatom a	
son 1		putref 1	uvar 3	putvar 3	
uref 1		putvar 2	up B_1	putstruct f/3	
son 2		putstruct g/2	A_1 :	check 1	bind
uvar 2		bind	putref 1	B_1 :	...

Code size can grow quite considerably — for **deep** terms. In practice, though, deep terms are “rare” :-)

31 Clauses

Clausal code must

- **allocate** stack space for locals;
- **evaluate** the body;
- **free** the stack frame (whenever possible :-)

Let r denote the clause: $p(X_1, \dots, X_k) \leftarrow g_1, \dots, g_n.$

Let $\{X_1, \dots, X_m\}$ denote the set of locals of r and ρ the address environment:

$$\rho X_i = i$$

Remark: The first k locals are always the **formals** :-)

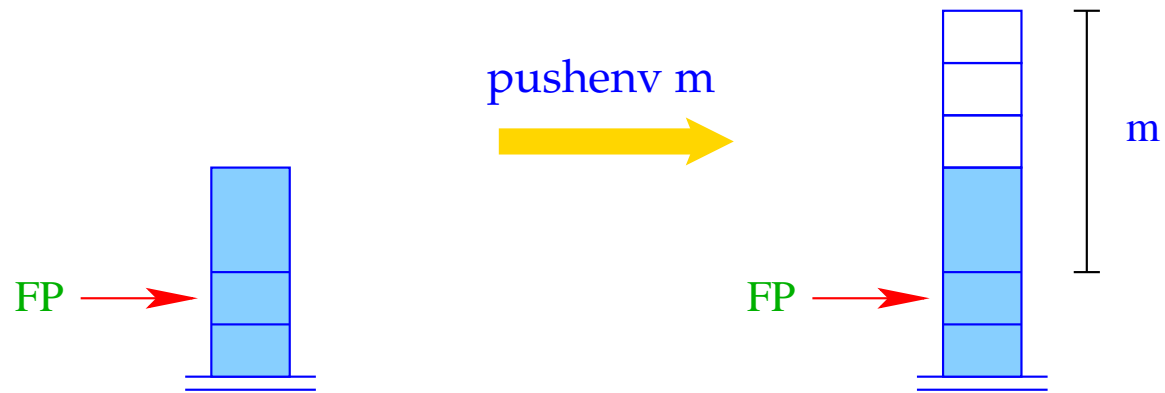
Then we translate:

```
codeC r = pushenv m           // allocates space for locals
          codeG g1 ρ
          ...
          codeG gn ρ
          popenv
```

The instruction `popenv` restores `FP` and `PC` and `tries to pop` the current stack frame.

It should succeed whenever program execution will never return to this stack frame :-)

The instruction `pushenv m` sets the stack pointer:



$$SP = FP + m;$$

Example:

Consider the clause r :

$$a(X, Y) \leftarrow f(\bar{X}, X_1), a(\bar{X}_1, \bar{Y})$$

Then `codeC r` yields:

pushenv 3

mark A

A: mark B

B: popenv

putref 1

putref 3

putvar 3

putref 2

call f/2

call a/2

32 Predicates

A predicate q/k is defined through a sequence of clauses $rr \equiv r_1 \dots r_f$.

The translation of q/k provides the translations of the individual clauses r_i .

In particular, we have for $f = 1$:

$$\text{code}_P rr = \text{code}_C r_1$$

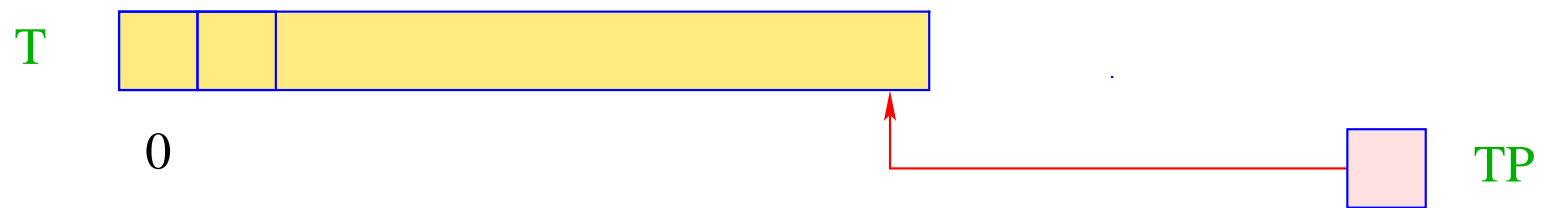
If q/k is defined through several clauses, the first alternative must be tried.

On failure, the next alternative must be tried

\implies backtracking :-)

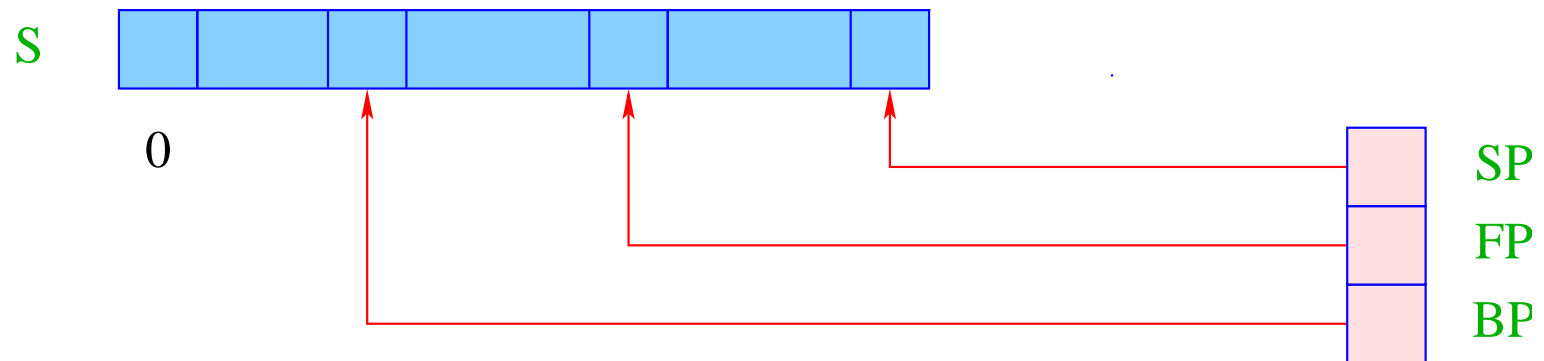
32.1 Backtracking

- Whenever unification fails, we call the run-time function `backtrack()`.
- The goal is to **roll back** the whole computation to the (**dynamically :-**) latest goal where another clause can be chosen \implies the last **backtrack point**.
- In order to undo intermediate variable bindings, we always have recorded new bindings with the run-time function `trail()`.
- The run-time function `trail()` stores variables in the data-structure **trail**:



TP == Trail Pointer
points to the topmost occupied Trail cell

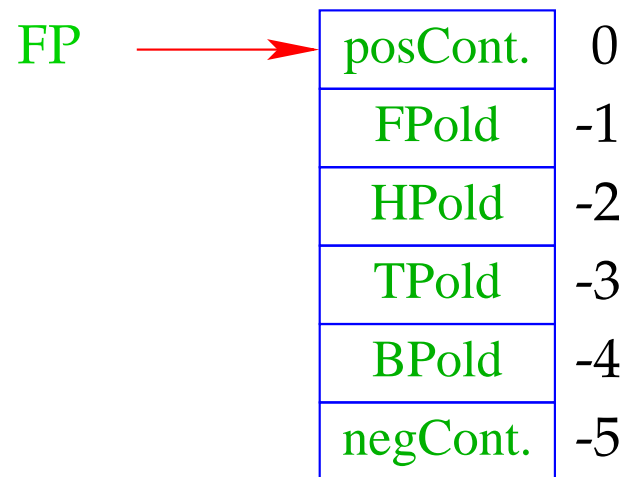
The current stack frame where backtracking should return to is pointed at by the extra register **BP**:



A **backtrack point** is stack frame to which program execution possibly returns.

- We need the code address for trying the **next** alternative (**negative continuation address**);
- We save the old values of the registers **HP**, **TP** and **BP**.
- **Note:** The **new BP** will receive the value of the current **FP** :-)

For this purpose, we use the corresponding four organizational cells:



For more comprehensible notation, we thus introduce the macros:

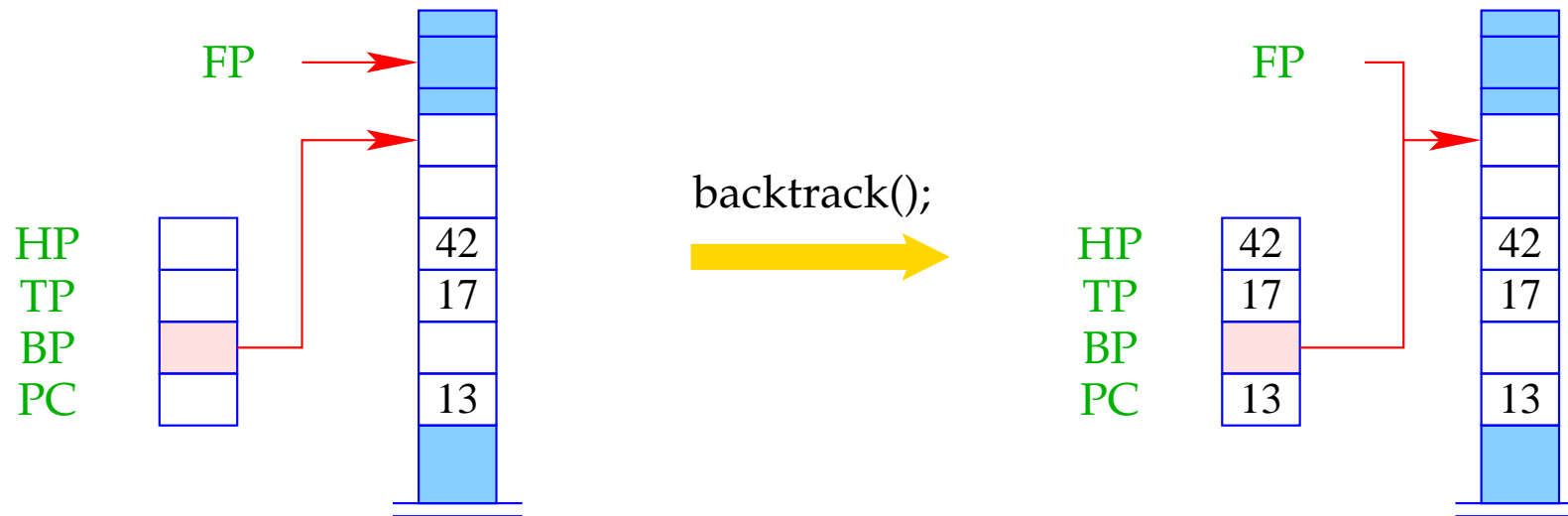
$$\begin{aligned}\text{posCont} &\equiv S[\text{FP}] \\ \text{FPold} &\equiv S[\text{FP} - 1] \\ \text{HPold} &\equiv S[\text{FP} - 2] \\ \text{TPold} &\equiv S[\text{FP} - 3] \\ \text{BPold} &\equiv S[\text{FP} - 4] \\ \text{negCont} &\equiv S[\text{FP} - 5]\end{aligned}$$

for the corresponding addresses.

Remark:

Occurrence on the **left** \equiv saving the register
Occurrence on the **right** \equiv restoring the register

Calling the run-time function `void backtrack()` yields:



```
void backtrack() {  
    FP = BP; HP = HPold;  
    reset (TPold, TP);  
    TP = TPold; PC = negCont;  
}
```

where the run-time function `reset()` undoes the bindings of variables established **since** the backtrack point.

32.2 Resetting Variables

Idea:

- The variables which have been created since the last backtrack point can be removed together with their bindings by popping the heap !!! :-)
- This works fine if **younger** variables always point to **older** objects.
- Bindings of **old** variables to younger objects, though, must be reset **manually** :-(
- These are therefore recorded in the trail.

Functions `void trail(ref u)` and `void reset (ref y, ref x)` can thus be implemented as:

```
void trail (ref u) {
    if (u < S[BP-2]) {
        TP = TP+1;
        T[TP] = u;
    }
}

void reset (ref x, ref y) {
    for (ref u=y; x<u; u--)
        H[T[u]] = (R,T[u]);
}
```

Here, `S[BP-2]` represents the heap pointer when creating the last backtrack point.

32.3 Wrapping it Up

Assume that the predicate q/k is defined by the clauses r_1, \dots, r_f ($f > 1$).

We provide code for:

- **setting** up the backtrack point;
- successively **trying** the alternatives;
- **deleting** the backtrack point.

This means:

```

codeP rr = q/k : setbtp
                try A1
                ...
                try Af-1
                delbtp
                jump Af
A1 : codeC r1
    ...
Af : codeC rf

```

Note:

- We delete the backtrack point **before** the last alternative **:-)**
- We **jump** to the last alternative — never to return to the present frame **:-))**

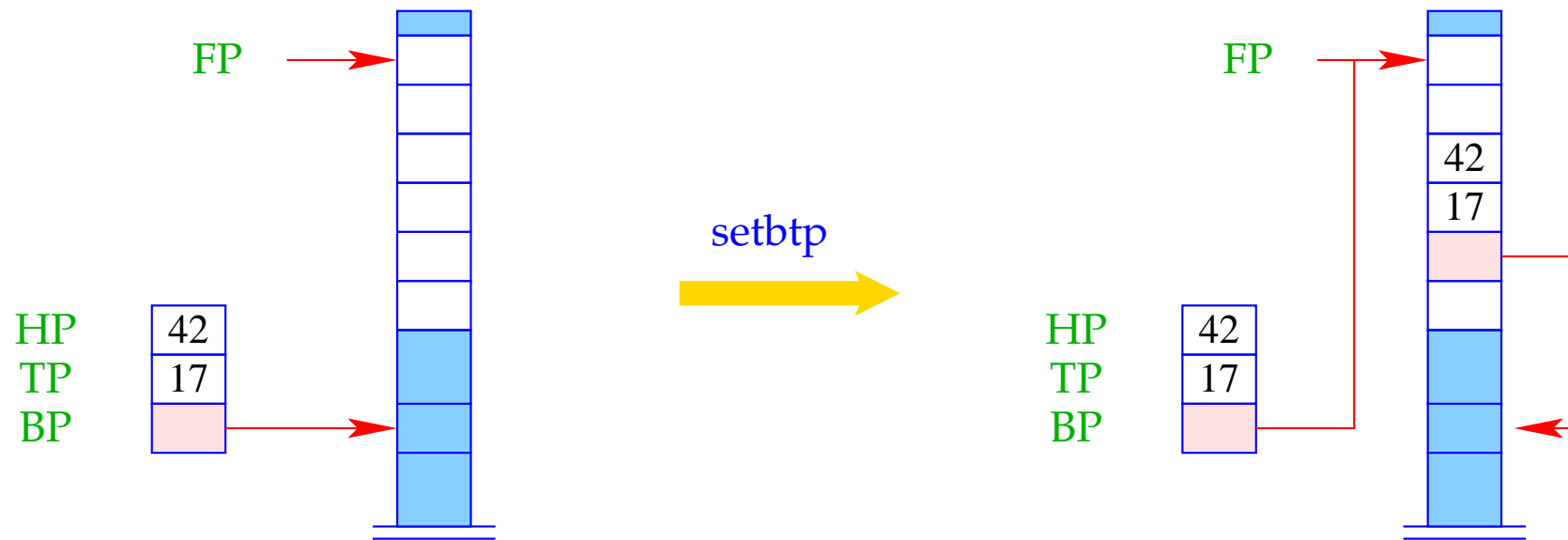
Example:

$$\begin{aligned} s(X) &\leftarrow t(\bar{X}) \\ s(X) &\leftarrow \bar{X} = a \end{aligned}$$

The translation of the predicate s yields:

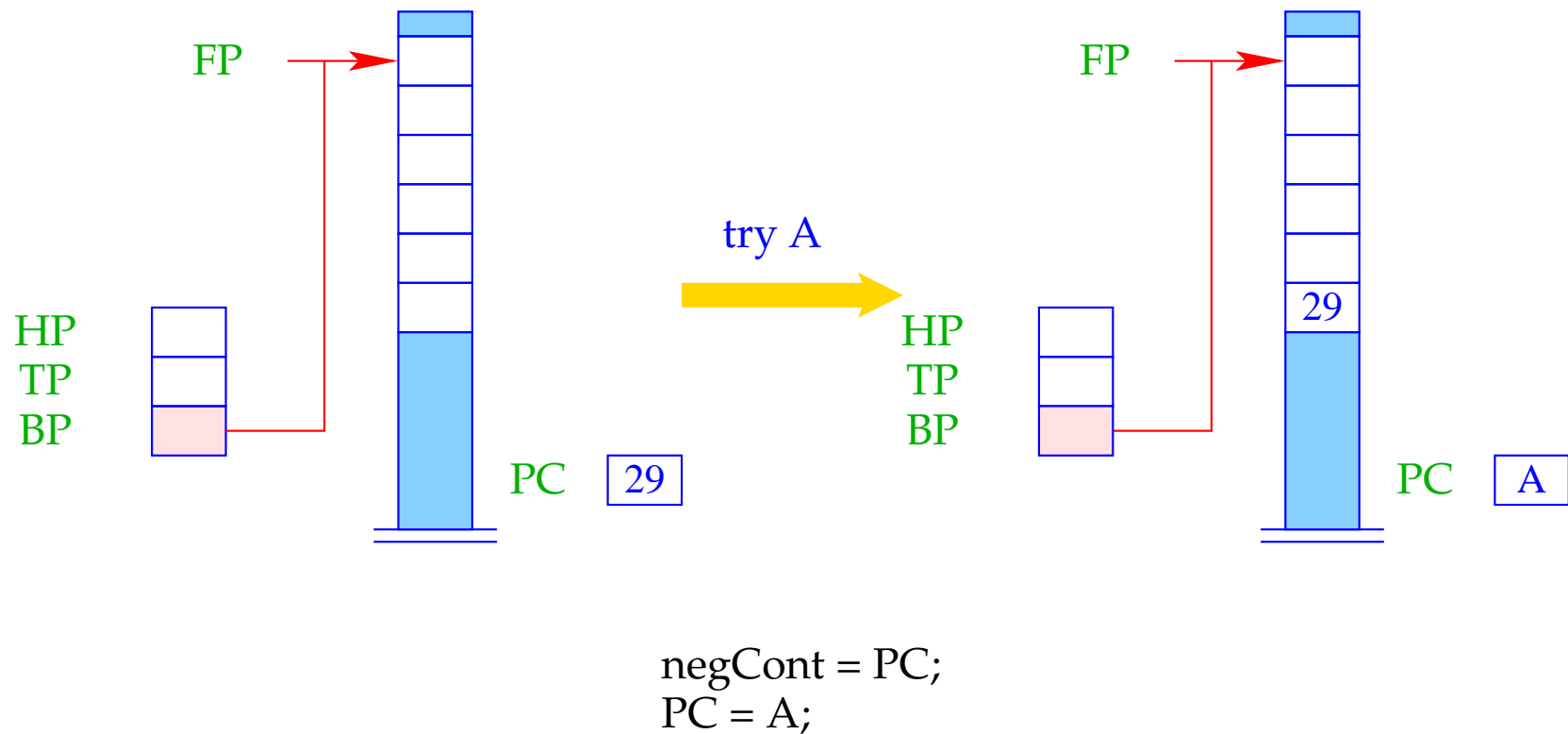
$s/1$:	setbtp	A:	pushenv 1	B:	pushenv 1
	try A		mark C		putref 1
	delbtp		putref 1		uatom a
	jump B		call t/1		popenv
		C:	popenv		

The instruction `setbtp` saves the registers `HP`, `TP`, `BP`:

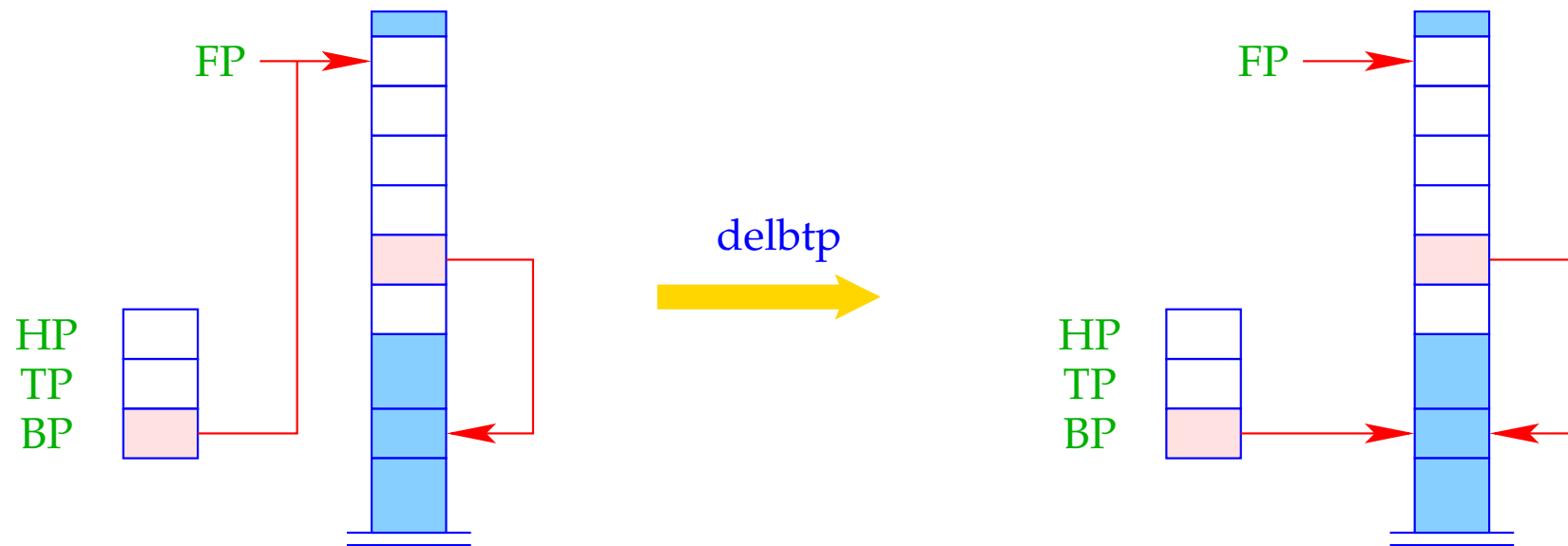


HPold = HP;
TPold = TP;
BPold = BP;
BP = FP;

The instruction `try A` tries the alternative at address `A` and updates the negative continuation address to the current `PC`:



The instruction `delbtp` restores the old backtrack pointer:



$BP = BP_{old};$